REMARKS/ARGUMENTS

Claims 1-5, 8-11, 14-21, 23, and 25 are pending in this application. Claims 1-5, 8-11, 14-21, 23, and 25 stand rejected. Claims 1, 8, 14, 18, 23 and 25 have been amended by this paper. Claims 27-31 have been added.

Claim Rejections - 35 USC § 103(a)

Claims 1-5, 8-11, 14-21, 23, and 25 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Gulick et al. (U.S. Pat. No. 6,314,501) in view of Vishin et al. (U.S. Pat. No. 5,860,146).

Applicant traverses this rejection in light of the claim amendments presented herein. In particular, claim 1 has been amended to require that "the mapping [between the plurality of physical resource identifiers and at least some of the plurality of machine resource identifiers] defines a non-monotonic function." Support for this limitation may be found, for example, in FIGS. 2A-2C of the present application. Consider, for example, physical memory blocks 212a, 212e, and 212d, as shown in FIG. 2A. The memory locations in these three memory blocks 212a, 212e, and 212d are sequentially numbered within the physical addresses 218a of the physical address space 202a. Referring to FIG. 2C, however, it may be seen that in the machine address space 202, the addresses of physical memory block 212e are higher than those of physical memory block 212a, while the addresses of physical memory block 212d are

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lower than those of physical memory block 212d. The mapping between

the physical addresses 218a (FIG. 2A) and the corresponding portion

of the machine addresses 216 (FIG. 2C) is therefore non-monotonic,

because the as the physical addresses 218a increase, the

corresponding ones of the machine addresses 216 first increase, and

then decrease.

Neither Gulick nor Vishin, nor the combination thereof, teaches

or suggests this express limitation of claim 1, as presently

amended. Gulick, for example, teaches a mapping that is monotonic.

Referring to FIG. 3 of Gulick, for example, the mapping between the

address space labeled "OS#1" and the corresponding addresses in MSU

memory space 350 is monotonic. As the addresses in address space

OS#1 increase, the corresponding addresses in the MSU memory space

350 also increase. Neither Gulick nor Vishin teaches or suggests

otherwise.

Claim 1, therefore, patentably distinguishes over the

combination of Gulick and Vishin. Claims 8, 14, and 18 have been

amended to include the same limitation and therefore patentably

distinguish over the combination of Gulick and Vishin for at least

the same reason. Claims 2-5, 9-11, and 14-21 depend from

corresponding ones of claims 1, 8, 14, and 18, and therefore

patentably distinguish over the combination of Gulick and Vishin for
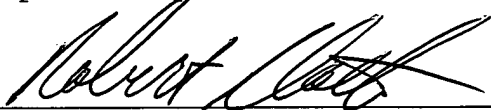
at least the same reason.

Claims 23 and 25 have been amended to include substantially the same limitation as discussed above, and therefore patentably distinguish over the combination of Gulick and Vishin for at least the reasons provided above.

Newly-added claims 27-31 are dependent claims which include the limitation discussed above by incorporation, and therefore patentably distinguish over the combination of Gulick and Vishin for at least the reasons provided above.
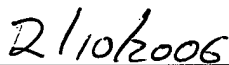
CONCLUSIONS

If this response is not considered timely filed and if a request for extension of time is otherwise absent, applicant hereby requests any extension of time. Please charge any fees or make any credits, to Deposit Account No. 08-2025.

Respectfully submitted,

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